



Chapter 1 : Introduction to Microcontroller Architecture

1.1	Introduction to Microprocessors.....	1-1
1.1.1	Some Basic Terms used in Microprocessors.....	1-1
1.1.2	Microprocessor Characteristics and Functions....	1-1
1.1.3	Block Diagram of a Microcomputer.....	1-2
1.1.4	Buses and Memory Accessing.....	1-4
1.1.4(A)	Address Bus.....	1-4
1.1.4(B)	Data Bus.....	1-5
1.1.4(C)	Control Bus.....	1-5
1.1.5	ALU (Arithmetic and Logic Unit).....	1-5
1.1.6	Basic Important Terms.....	1-6
1.2	Introduction to Microcontroller.....	1-6
1.2.1	Microprocessor.....	1-6
1.2.2	Microcontroller.....	1-6
1.2.3	Comparison of Microprocessors and Microcontrollers.....	1-7
1.2.4	Advantages of Microcontrollers over Microprocessors.....	1-7
1.2.5	Disadvantages/Limitations of 8 bit Microcontroller over Microprocessors.....	1-7
1.2.6	Applications of Microcontrollers.....	1-8
1.3	Harvard and Von Neumann Architectures.....	1-8
1.3.1	Von Neumann Architecture.....	1-8
1.3.1(A)	Key Features of a Von Neumann Machine.....	1-9
1.3.1(C)	Execution of a Program by Von Neumann Machine.....	1-10
1.3.1(D)	Fetching an Instruction.....	1-10
1.3.2	Comparison between Von Neumann and Harvard Architecture.....	1-10

1.3.3	Harvard Architecture of PIC Microcontroller.....	1-11
1.4	Microcontrollers and Embedded Processors.....	1-11
1.5	RISC and CISC.....	1-12
1.5.1	Properties of RISC.....	1-12
1.5.2	Register Window.....	1-12
1.5.3	Miscellaneous Features / Advantages of RISC Systems.....	1-13
1.5.4	RISC Shortcomings.....	1-14
1.6	Criteria for Selecting a Microcontroller.....	1-15
1.7	Performance of Microcontroller.....	1-16
1.8	Embedded System.....	1-16
1.8.1	Characteristics of Embedded Systems.....	1-17
1.8.2	Role of Microcontroller in Embedded System.....	1-18
1.9	Introduction to 8051.....	1-18
1.9.1	Features of 8051.....	1-19
1.9.2	Other Members of 8051 Family.....	1-19
1.9.3	Architecture of 8051.....	1-19
1.9.3(A)	Accumulator (ACC).....	1-19
1.9.3(B)	B Register.....	1-19
1.9.3(C)	Arithmetic and Logic Unit (ALU).....	1-20
1.9.3(D)	Program Status Word.....	1-20
1.9.3(E)	Clock and Oscillator.....	1-21
1.9.3(F)	Stack Pointer.....	1-22
1.9.3(G)	Stack and Bank 1 Conflict.....	1-22
1.9.3(H)	Data Pointer.....	1-23
1.9.3(I)	Ports 0 to 3.....	1-23
1.9.3(J)	Timers and Counters.....	1-23
1.9.3(K)	Serial Data Buffer.....	1-23
1.9.3(L)	on-Chip Oscillator.....	1-23



1.9.3(M) Program Counter (PC)	1-23	1.14	Timers and Counters of 8051.....	1-32
1.10 Internal RAM Organization and Register Structure	1-23	1.14.1	Timer Mode Control (TMOD) Special Function Register.....	1-32
1.10.1 Special Function Registers (SFRs).....	1-23	1.14.2	Timer Control (TCON) Special Function Register	1-33
1.10.2 Four Register Banks of 8 Bytes.....	1-25	1.14.3	Timer Mode 0	1-33
1.10.3 External Memory Interface.....	1-25	1.14.4	Timer Mode 1	1-34
1.11 Programmers Model of 8051.....	1-26	1.14.5	Timer Mode 2	1-34
1.12 Pin Functions of 8051	1-26	1.14.6	Timer Mode 3	1-34
1.13 Ports Structure of 8051	1-28	1.15	Serial Interface of 8051.....	1-35
1.13.1 Port 0	1-29	1.15.1	Serial Port Modes.....	1-36
1.13.1(A) Reading from a Port Pin.....	1-29	1.15.1(A)	Mode 0 : Shift Register Mode	1-36
1.13.1(B) Writing to a Port	1-30	1.15.1(B)	Mode 1 : Standard UART	1-36
1.13.1(C) Port Loading and Interfacing.....	1-30	1.15.1(C)	Mode 2 : Multiprocessor Mode.....	1-37
1.13.1(D) Read-Modify-Write Feature	1-30	1.15.1(D)	Mode 3 : Multiprocessor Mode.....	1-37
1.13.2 Port 1	1-30	1.15.2	Baud Rates.....	1-37
1.13.2(A) Reading from a Port Pin.....	1-30	1.15.2(A)	Mode 0.....	1-37
1.13.2(B) Writing to a Port	1-30	1.15.2(B)	Modes 1 and 3.....	1-37
1.13.2(C) Port Loading and Interfacing.....	1-30	1.15.2(C)	Mode 2	1-38
1.13.2(D) Read Modify Write Feature	1-30	1.15.3	The Serial port Control (SCON) Special Function Register.....	1-38
1.13.3 Port 2	1-30	1.16	Power Saving Modes.....	1-39
1.13.3(A) Reading from a Port Pin.....	1-31	1.16.1	The Power Mode Control (PCON) Special Function Register	1-39
1.13.3(B) Writing to a Port	1-31	1.16.2	Power Saving Modes of Operation	1-39
1.13.3(C) Port Loading and Interfacing.....	1-31	1.16.2(A)	Power Down Operation.....	1-39
1.13.3(D) Read Modify Write Feature	1-31	1.16.2(B)	Idle Mode Operation.....	1-39
1.13.4 Port 3	1-31	1.17	Interrupts Structure of 8051	1-40
1.13.4(A) Reading from a Port Pin.....	1-31	1.17.1	8051 Interrupts and their Priority	1-40
1.13.4(B) Writing to a Port	1-31	1.17.2	Vector Addresses of 8051 Interrupts.....	1-40
1.13.4(C) Port Loading and Interfacing.....	1-32			
1.13.4(D) Read-Modify-Write Feature	1-32			



1.17.3	Enabling and Disabling of Interrupts	1-41
1.17.4	Interrupt Priority.....	1-41
1.17.5	Timer Flag Interrupt.....	1-41
1.17.6	Serial Port Interrupt.....	1-41
1.17.7	External Interrupts.....	1-41
1.17.8	Reset is a Non-Maskable Interrupts.....	1-42
1.18	Addressing Modes.....	1-42
1.18.1	Immediate Addressing.....	1-42
1.18.2	Direct Addressing.....	1-42
1.18.3	Indirect Addressing.....	1-43
1.18.4	Register Addressing.....	1-43
1.18.5	External Indirect.....	1-43
1.19	Summary of 8051 Instructions.....	1-44
1.19.1	Arithmetic Operators.....	1-44
1.19.2	Logical Operations	1-45
1.19.3	Data Transfer.....	1-45
1.19.4	Boolean Manipulation	1-46
1.20	Programs on Basic Operations	1-47
1.21	Exam Pack (Review and University Questions)	1-51

Chapter 2 : Programming Timer/Counter, Serial Port and Interrupts of 8051 **2-1 to 2-8**

2.1	Programming the 8051 Timer and I/O Ports.....	2-1
2.1.1	Programming the Timer in Mode 1	2-1
2.1.2	Mode 0 Programming.....	2-3
2.1.3	Programming the Timer in Mode 2	2-3
2.1.4	Counter and Pulse Width Measurement (PWM)	2-4
2.2	Interrupt Service Routines	2-4
2.3	Serial Communication Programming	2-6

2.3.1	Programming the 8051 to Transfer Data Serially.....	2-6
2.3.2	Programming the 8051 to Receive Data Serially.....	2-7
2.4	Exam Pack (Review and University Questions) ...	2-8

Chapter 3 : 8051 Programming in Embedded C **3-1 to 3-25**

3.1	Programming in Embedded C.....	3-1
3.1.1	Bit Addressable I/O Programming.....	3-1
3.1.2	Data Types using in C Programming for 8051.....	3-1
3.1.3	Data Serialization	3-3
3.2	Programming in C	3-3
3.2.1	Bit Addressable I/O Programming	3-4
3.3	Data Types using in C Programming for 8051.....	3-4
3.4	Checksum Byte	3-8
3.5	Data Serialization.....	3-9
3.6	8051 Hardware Connection.....	3-10
3.6.1	Intel Hex File.....	3-11
3.7	Design Problems.....	3-11
3.8	Exam Pack (Review and University Questions).....	3-25

Chapter 4 : 8051 I/O Port Interfacing - I **4-1 to 4-38**

4.1	Interfacing DAC, ADC and Sensors.....	4-1
4.2	D/A Converters	4-1
4.3	A/D Converters	4-2
4.4	Interfacing ADC and DAC to MCS-51 Family.....	4-4
4.5	ADC 0804	4-5
4.5.1	Features of ADC 0804	4-5
4.5.2	Pin Configuration and Functional Block Diagram	4-5



4.5.3	Interfacing ADC 0804 to MCS-51 Family	4-6
4.6	MAX 1112/MAX 1113	4-8
4.6.1	Pin Description	4-8
4.6.2	Control-Byte Format	4-9
4.6.3	Timing Diagram.....	4-9
4.6.4	Interfacing 8051 to MAX 1112/MAX 1113.....	4-10
4.7	Temperature Sensor LM35.....	4-11
4.8	Design Problems	4-12
4.9	Interfacing Relays.....	4-17
4.10	Solid State Relay.....	4-18
4.11	Optoisolator	4-18
4.12	Interfacing Stepper Motor.....	4-19
4.13	Interfacing of DC Motor	4-21
4.14	Interfacing DC Motor using L293 H-Bridge.....	4-21
4.15	Design Problems	4-26
4.16	Keyboard	4-31
4.16.1	Key Switch Mechanism.....	4-31
4.16.2	Hardware Key Debouncing.....	4-32
4.16.3	Software Key Debouncing.....	4-32
4.17	Keyboard Interface Circuit.....	4-32
4.17.1	Non-matrix Type Keyboard.....	4-32
4.17.2	Matrix Keyboard Interface.....	4-33
4.18	Roll-Over Techniques.....	4-34
4.19	Display	4-35
4.19.1	LED Displays	4-36
4.19.2	Seven Segment Display (SSD)	4-36
4.19.3	Interfacing Multiple Seven Segment Displays	4-37
4.20	Exam Pack (Review and University Question)	4-38

Chapter 5 : PIC 18FXXXX Microcontroller Architecture

5-1 to 5-26

5.1	Introduction.....	5-1
5.2	Comparison of CISC and RISC Processors	5-1
5.3	Harvard and Von Neumann Architectures	5-2
5.3.1	Harvard Architecture of PIC Microcontroller.....	5-2
5.4	Features of PIC Family of Microcontrollers : Overview of PIC Microcontroller Family 12FXX, 16FXX and 18FXX	5-2
5.4.1	PIC Microcontroller Features	5-3
5.5	Comparison of Features of PIC10, PIC12, PIC16, PIC18 Families.....	5-4
5.6	PIC18F458 Features	5-5
5.7	Pin Diagram of PIC18F458.....	5-7
5.8	Architecture of PIC18F458 (Block Diagram)	5-7
5.8.1	Program Memory	5-8
5.8.2	31 Level Stack.....	5-8
5.8.3	Data RAM.....	5-9
5.8.4	Arithmetic and Logic Unit (ALU)	5-9
5.8.5	CPU Registers	5-9
5.8.5(A)	Working Register (WREG).....	5-9
5.8.5(B)	Status Register	5-9
5.8.5(C)	Program Counter and PCLATH.....	5-10
5.8.5(D)	Stack	5-10
5.8.5(E)	INDF and FSR	5-11
5.8.6	8 × 8 Multiply Unit.....	5-11
5.8.7	Five I/O (Ports A, B, C, D, E)	5-11
5.8.8	Four Timers (Timer0 to Timer3)	5-11
5.8.8(A)	Applications of Timer in PIC18F.....	5-12
5.8.9	Analog to Digital Convertor (ADC)	5-12



5.8.10	Analog Comparator	5-12	5.12	Pipelining	5-22
5.8.11	Master Synchronous Serial Port (MSSP).....	5-13	5.13	Power-Down Mode (Sleep)	5-23
5.8.11(A)	Serial Peripheral Interface (SPI) Mode.....	5-13	5.13.1	Wake-Up from Sleep.....	5-24
5.8.11(B)	I2C (Inter Integrated Circuit) Mode	5-13	5.14	Exam Pack (Review and University Questions)	5-24
5.8.12	USART Ports.....	5-13	<hr/>		
5.8.13	Interrupt and its Handling in PIC18F	5-13	Chapter 6 : Overview of Instruction Set 6-1 to 6-58		
5.9	Oscillator Configurations	5-14	6.1	Addressing Modes for PIC18 Microcontroller	6-1
5.9.1	HS, HS4, XT or LP Oscillator	5-15	6.1.1	Register Direct.....	6-1
5.9.2	RC Oscillator Mode	5-16	6.1.2	Immediate Mode.....	6-2
5.9.3	EC and ECIO Oscillator Modes.....	5-16	6.1.3	Inherent Mode	6-2
5.9.4	Instruction Cycle.....	5-16	6.1.4	Indirect Mode.....	6-2
5.10	PIC18 RESET	5-17	6.1.5	Bit-Direct Addressing Mode.....	6-4
5.10.1	Power-on Reset (POR).....	5-18	6.2	Instruction Set.....	6-4
5.10.2	MCLR.....	5-18	6.3	Data Transfer Instructions	6-7
5.10.3	Power-up Timer (PWRT)	5-18	6.3.1	MOVF or MOVE MOVF f,d,a.....	6-7
5.10.4	Oscillator Start-up Timer (OST)	5-18	6.3.2	MOVFF	6-7
5.10.5	PLL Lock Time-out.....	5-18	6.3.3	MOVLB.....	6-8
5.10.6	Brown-Out Reset (BOR).....	5-18	6.3.4	MOVLW K.....	6-8
5.10.7	Time-Out Sequence	5-19	6.3.5	MOVWF	6-9
5.10.8	Reset at Supply Voltage Drop below the Permissible (Brown-out Reset).....	5-19	6.3.6	SWAPF	6-9
5.10.9	Watchdog Timer	5-19	6.3.7	LFSR.....	6-9
5.11	Program and Data Memory Organization	5-20	6.3.8	PUSH.....	6-10
5.11.1	Data Memory	5-20	6.3.9	POP	6-10
5.11.1(A)	Bank Select Register (BSR).....	5-21	6.3.10	CLRF.....	6-10
5.11.2	Program Memory Organization.....	5-21	6.3.11	SETF.....	6-11
			6.4	Arithmetic Instructions	6-11
			6.4.1	ADDLW	6-11
			6.4.2	ADDWF	6-11



6.4.3	ADDWFC.....	6-12	6.6.1	RLCF.....	6-24
6.4.4	DAW.....	6-12	6.6.2	RLNCF.....	6-25
6.4.5	SUBLW.....	6-13	6.6.3	RRCF.....	6-25
6.4.6	SUBWF.....	6-14	6.6.4	RRNCF.....	6-26
6.4.7	SUBWFB.....	6-15	6.7	Branch Instructions.....	6-26
6.4.8	SUBFWB.....	6-16	6.7.1	BC.....	6-26
6.4.9	INCF.....	6-16	6.7.2	BN.....	6-27
6.4.10	INCFSZ.....	6-17	6.7.3	BNC.....	6-27
6.4.11	INCFSNZ.....	6-17	6.7.4	BNN.....	6-28
6.4.12	DECF.....	6-18	6.7.5	BNOV.....	6-28
6.4.13	DECFSZ.....	6-18	6.7.6	BNZ.....	6-28
6.4.14	DECFSNZ.....	6-18	6.7.7	BOV.....	6-29
6.4.15	MULLW.....	6-19	6.7.8	BRA.....	6-29
6.4.16	MULWF.....	6-19	6.7.9	GOTO.....	6-29
6.5	Logical Instructions.....	6-20	6.7.10	BZ.....	6-30
6.5.1	ANDLW.....	6-20	6.8	CALL and RETURN Instructions.....	6-30
6.5.2	ANDWF.....	6-20	6.8.1	CALL.....	6-30
6.5.3	IORLW.....	6-21	6.8.2	RCALL.....	6-31
6.5.4	IORWF.....	6-21	6.8.3	RETURN.....	6-31
6.5.5	XORLW.....	6-21	6.8.4	RETFIE.....	6-31
6.5.6	XORWF.....	6-22	6.8.5	RETLW.....	6-32
6.5.7	COMF.....	6-22	6.9	Table Read/Write Instructions.....	6-32
6.5.8	NEGF.....	6-22	6.9.1	TBLRD*.....	6-32
6.5.9	CPFSEQ.....	6-23	6.9.2	TBLRD*+.....	6-33
6.5.10	CPFSGT.....	6-23	6.9.3	TBLRD*-.....	6-34
6.5.11	CPFSLT.....	6-24	6.9.4	TBLRD+*.....	6-34
6.5.12	TSTFSZ.....	6-24	6.9.5	TBLWT*.....	6-35
6.6	Rotate Instructions.....	6-24	6.9.6	TBLWT*+.....	6-36



6.9.7	TBLWT*-	6-37	6.18.3	Binary Coded Decimal (BCD) Addition.....	6-53
6.9.8	TBLWT+*	6-37	6.18.4	Multiplication	6-54
6.10	Machine Control Instructions.....	6-38	6.19	Program Loops	6-55
6.10.1	CLRWDT.....	6-38	6.19.1	Changing the Program Counter	6-55
6.10.2	NOP.....	6-38	6.20	Exam Pack	
6.10.3	SLEEP	6-39		(Review and University Questions)	6-57
6.10.4	RESET	6-39	<hr/>		
6.11	Bit Addressable Instructions	6-39	Chapter 7 : Peripheral Support in PIC 18FXXXX – I		
6.11.1	BCF.....	6-39	7-1 to 7-14		
6.11.2	BSF	6-40	7.1	CCP Module in PIC18 Microcontroller	7-1
6.11.3	BTFSC	6-40	7.2	Timers Required for CCP Applications.....	7-1
6.11.4	BTFSS.....	6-41	7.3	CCP Registers	7-1
6.11.5	BTG.....	6-41	7.3.1	CCP1CON Control Register	7-1
6.12	Assembly Language Programs.....	6-41	7.4	Generating of Waveform using Compare Mode of CCP Module	7-2
6.13	Development Tools for PIC Microcontrollers.....	6-42	7.4.1	Programming the CCP Module in Compare Mode	7-3
6.14	MPLAB Integrated Development Environment Software	6-42	7.5	Capture Mode.....	7-4
6.14.1	Assembler (MPASM)	6-43	7.6	PWM Mode	7-6
6.14.2	Software Simulator (MPLAB-SIM)	6-43	7.6.1	Period of PWM.....	7-6
6.14.3	C Compiler (MPLAB-C)	6-43	7.6.2	Duty Cycle of PWM.....	7-7
6.15	Introduction to Assembly Language Programming and its Structure.....	6-44	7.6.3	Steps for Programming the CCP Module for PWM Generation.....	7-7
6.15.1	Assembly Language Program Structure.....	6-44	7.7	Speed Control of DC Motor using PWM Mode of CCP Module.....	7-9
6.16	Assembler Directives.....	6-44	7.7.1	Interfacing DC Motor using L293 H-Bridge and Opto-isolator	7-10
6.17	Writing Simple Programs	6-47	7.8	Exam Pack	
6.18	Writing Programs to Perform Arithmetic Computations.....	6-49		(Review and University Questions)	7-13
6.18.1	Perform Addition Operations.....	6-49	<hr/>		
6.18.2	Perform Subtraction Operations.....	6-51			

**Chapter 8 : Peripheral Support in PIC 18FXXXX – II****8-1 to 8-8**

8.1	Interrupts Vs Polling.....	8-1
8.2	Interrupt Service Routine (ISR)	8-1
8.3	Steps in Executing an Interrupt.....	8-2
8.4	PIC18 Interrupt Structure.....	8-3
8.4.1	Timer Flag Interrupts.....	8-3
8.4.1(A)	PIR Registers	8-3
8.4.1(B)	Interrupt Control Register	8-3
8.5	PIC18 Interrupt Programming in C Using C18 Compiler	8-5
8.6	Hardware Interrupts	8-6
8.7	Serial Port Interrupts	8-7
8.8	Setting the Interrupt Priority	8-7
8.9	Interrupt inside an Interrupt /Nested Interrupt.....	8-8
8.10	Exam Pack (Review and University Questions)	8-8

Chapter 9 : Peripheral Support in PIC 18FXXXX – III**9-1 to 9-21**

9.1	Timers / Counters.....	9-1
9.2	Prescaling of PIC Timers.....	9-1
9.3	Timer 0	9-1
9.3.1	Timer 0 Block Diagram.....	9-1
9.3.2	Timer 0 Registers	9-3
9.3.3	TOCON (Timer 0 Control) Register	9-3
9.4	Timer 1	9-4
9.4.1	Timer 1 Block Diagram.....	9-4
9.4.2	Timer 1 Registers	9-6

9.4.3	Timer 1 Control Register (T1CON).....	9-6
9.5	Timer 2	9-7
9.5.1	Timer 2 Block Diagram.....	9-8
9.5.2	Timer2 Registers and TMR2IF Flag.....	9-8
9.5.3	Timer 2 Control Register (T2CON).....	9-9
9.6	Timer 3	9-9
9.6.1	Timer 3 Block Diagram.....	9-10
9.6.2	Timer 3 Control Register (T3CON).....	9-11
9.7	Programming the PIC18 Timers	9-12
9.8	Exam Pack (Review and University Questions).....	9-20

Chapter 10 : Real World Interfacing with PIC18FXXXX - I**10-1 to 10-22**

10.1	Interfacing DAC	10-1
10.1.1	Features of DAC 0808	10-1
10.1.2	Pin Configuration and Functional Block Diagram.....	10-1
10.1.3	Interfacing DAC with PIC18F458.....	10-3
10.1.4	Programming PIC to Interface with DAC0808....	10-3
10.2	PIC18F458 ADC.....	10-5
10.2.1	ADCON0 Register	10-5
10.2.2	ADCON1 Register	10-6
10.2.3	A/D Conversion Time.....	10-7
10.3	A/D Acquisition Requirements	10-9
10.4	Selecting the A/D Conversion Clock	10-10
10.5	Configuring Analog Port Pins	10-10
10.6	Use of the ECCP Trigger	10-11



13.10.1	PIC18F SPI Bus.....	13-11	13.12.5	Baud Rate Generator	13-21
13.10.2	Registers	13-12	13.13	Comparison of SPI and I ² C	13-22
13.11	I ² C Bus.....	13-14	13.14	Exam Pack	
13.11.1	Comparison between RS232, RS485, I ² C.....	13-15		(Review and University Questions)	13-22
13.12	I ² C Mode.....	13-15	<hr/>		
13.12.1	Registers	13-16	Chapter 14 : Serial Port Programming interfacing with		
13.12.2	Operation.....	13-18	PIC18FXXXX - II		
13.12.3	Slave Mode.....	13-18	14-1 to 14-9		
13.12.3(A)	Addressing.....	13-19	14.1	Features of DS1306.....	14-1
13.12.3(B)	Reception	13-19	14.2	Pin Functions of DS1306	14-1
13.12.3(C)	Transmission.....	13-20	14.3	Control Registers and Operation of DS1306.....	14-3
13.12.4	Master Mode	13-20	14.4	Programming and Interfacing DS1306 with PIC Microcontroller	14-6
			14.5	Interfacing DS1307 with PIC18.....	14-6
			14.6	Exam Pack	
				(Review and University Questions)	14-9